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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No. NOT YET ASSIGNED

Title: **WIDE ANGLE IMAGE PICKUP APPARATUS**

TRANSMITTAL LETTER

The Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Submitted herewith for filing in the U.S. Patent and Trademark  
Office is the following:

- (1) Transmittal Letter;
- (2) PTO Form-1082;
- (3) 1 Page of the Abstract of the Disclosure  
23 Page Application consisting of:  
15 Pages Textual Specification  
2 Pages of 5 claims  
5 Sheets of 10 Drawing
- (4) Executed Declaration and Power of Attorney
- (5) Assignment and Recordation Cover Sheet
- (6) Request for Priority
- (7) Priority Document No. P11-251285; Dated September 6, 1999
- (8) Check No. 13352 for \$690.00 for Government filing fee
- (9) Check No. 13353 for \$40.00 for Assignment Recordation
- (10) Postcard for early notification of Serial Number.

Respectfully submitted,

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## FORM PTO-1082

### TRANSMITTAL FOR NEW U.S. PATENT APPLICATION

Assistant Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

Re: New U.S. Patent Application  
Title: WIDE ANGLE IMAGE PICKUP APPARATUS  
Inventor(s): Kenji MATSUOKA, Kenji TAGUCHI, Nobuyuki  
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Attorney Docket: 24369

Sir:

Attached hereto is the application identified above, including:

23 Page Application Consisting of:  
    15 Pages of Textual Specification  
    2 Pages of 5 Claims  
    1 Abstract of the Disclosure  
    5 Sheets of Drawings  
x Executed Inventor's Declaration  
\_\_\_ Unexecuted Inventor's Declaration  
\_\_\_ Small Entity Statement  
x Assignment and cover sheet

The Government filing fee\* is calculated as follows:

Total Claims . . . . .	<u>5</u> - 20 = <u>0</u>	x \$18.00 = <u>\$0.00</u>
Independent Claims . . . . .	<u>2</u> - 3 = <u>0</u>	x \$78.00 = <u>\$0.00</u>
Base Fee . . . . .		<u>\$690.00</u>
Multiple Dependent Claim Fee (\$260.00) . . . . .		<u>\$ 0.00</u>
Subtotal . . . . .		<u>\$690.00</u>

**TOTAL FILING FEE\***

(accounting for possible small entity status) . . . **\$690.00**

\_\_\_ Reduced by one-half, as applicant(s) is/are a "small entity".  
A Declaration Claiming Small Entity Status:

\_\_\_ is submitted herewith;

\_\_\_ will be filed at a later date.

X Foreign priority is claimed under 35 U.S.C. § 119 from  
Japanese Patent Application(s) No. P11-251285 dated September  
6, 1999.

\_\_\_ Priority document(s) will be submitted at a later date.

X Priority document(s) is/are submitted herewith.

\_\_\_ Executed Declaration(s) will be submitted at a later date  
pursuant to 37 CFR § 1.41 and § 1.53, with an appropriate  
surcharge under 37 CFR § 1.16(e).

X Formal drawing(s) is/are attached.

\_\_\_ Formal drawing(s) will be submitted at a later date.

X Assignment document(s) is/are submitted herewith; the recorda  
tion fee of \$40.00 per document is enclosed herewith.

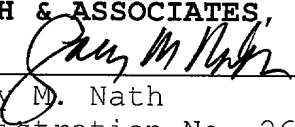
\_\_\_ Assignment document will be submitted at a later date.

\_\_\_ A Verified Translation will be submitted at a later date.

\_\_\_ No payment is enclosed at this time. Full payment will be  
made when the executed Declaration is submitted.

X Submitted herewith is a check in the amount of \$690.00.  
Please charge any required fee, or credit any overpayment, in  
connection with this matter to deposit Account No. 14,0112.

Respectfully submitted,  
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# WIDE ANGLE IMAGE PICKUP APPARATUS

## BACKGROUND OF THE INVENTION

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### 1. Field of the Invention

The present invention relates to a wide angle image pickup apparatus preferable for use in a camera having a solid-state image device of frame transfer type.

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### 2. Description of the Related Art

An electronic zoom circuit loaded on a camera integrated type VTR is used to obtain an enlarged magnification image which cannot be covered sufficiently by an optical zoom alone, together with the optical zoom. That is, an enlarged image on a telescope which cannot be obtained by the optical zoom alone is generated and secured by interpolation processing between pixels.

15

On the contrary, on the side of a wide angle in the zoom lens region of the camera integrated type VTR, even if an image is subjected to reduction processing by electronic zoom, only a black belt appears in the periphery of the image. Therefore, generally such an image reduction processing is not carried out. Thus, the angle of view on the side of the wide angle in the camera integrated type VTR is fixed depending on a design of the optical zoom lens.

20

25

## SUMMARY OF THE INVENTION

Accordingly, if it is intended to enlarge the angle of view on the side of the wide angle in the camera integrated type VTR, the zoom lens is likely to be enlarged because of its design matter, which is a serious problem for a home use camera integrated type VTR. Thus, according to a conventional technology, the angle of view on the side of the wide angle cannot be enlarged so much.

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To solve the above problem, there is provided a wide angle image pickup apparatus comprising: a solid-state image device containing a standard image pickup region in which image pickup devices of a number corresponding to the number  
5 of horizontal scanning lines of a video image signal in accordance with standard television system are arranged in a vertical direction and a peripheral image pickup region which is provided at least on vertically top and vertically bottom of the standard image pickup region and in which  
10 multiple image pickup devices are arranged in the vertical direction; and a reduction processing means for reducing a pixel signal outputted from the standard image pickup region and peripheral image pickup region of the solid-state image device so as to output a video signal having the number of  
15 the horizontal scanning lines.

According to a preferred embodiment of the present invention, the solid-state image device reads out a pixel signal from the standard image pickup region and peripheral image pickup region and the reduction processing means  
20 contains a memory means in which a pixel signal outputted from the solid-state image device is written and read out synchronously with a vertical synchronous signal of the video signal.

According to a preferred embodiment of the present  
25 invention, normal image pickup mode and reduction image pickup mode are further included, wherein under the normal image pickup mode, a pixel signal outputted from the standard image pickup region of the solid-state image device is outputted as the video signal without the reduction  
30 processing by the reduction processing means, and under the reduction image pickup mode, a pixel signal outputted from the standard image pickup region and peripheral image pickup region of the solid-state image device is outputted as the video signal with the reduction processing by the reduction  
35 processing means.

According to a preferred embodiment of the present

invention, the memory means is capable of carrying out write and read of the pixel signal at the same time and under the reduction image pickup mode, the solid-state image device starts output of pixel signals corresponding to a screen at such a timing that a output completion timing of the pixel signal of a screen coincides with a timing of a vertical synchronous signal of the video signal.

Further, to solve the above problem, there is provided a wide angle image pickup apparatus having a solid-state image device containing a standard image pickup region in which image pickup devices of a number corresponding to the number of horizontal scanning lines of a video image signal in accordance with standard television system are arranged in a vertical direction and a peripheral image pickup region which is provided at least on vertically top and vertically bottom of the standard image pickup region and in which multiple image pickup devices are arranged in the vertical direction, the wide angle image pickup apparatus further containing a moving image output mode for outputting the video signal which is a moving image based on a pixel signal outputted from at least the standard image pickup region of the solid-state image device and a still image output mode for outputting pixel signals corresponding to a screen outputted from the standard image pickup region and peripheral image pickup region of the solid-state image device.

The nature, principle and utility of the invention will become more apparent from the following detailed description when read in conjunction with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

In the accompanying drawings:

FIG.1 is a diagram for explaining a wide angle image pickup apparatus according to a first embodiment of the present invention;

FIG.2 is a diagram showing an imaging plane of a solid-state image device;

FIG.3 is a diagram for explaining an operation of the wide angle image pickup apparatus according to the first embodiment of the present invention;

FIG.4 is a diagram for explaining an operation of the wide angle image pickup apparatus according to the second embodiment of the present invention;

FIG.5 is a diagram for explaining an operation of the wide angle image pickup apparatus according to the second embodiment of the present invention;

FIG.6 is a diagram showing a write address and read address for a memory in the wide angle image pickup apparatus according to the second embodiment of the present invention;

FIG.7 is a diagram for explaining an operation of the wide angle image pickup apparatus according to a third embodiment of the present invention;

FIG.8 is a diagram showing a write address and read address for a memory in the wide angle image pickup apparatus according to the third embodiment of the present invention;

FIG.9 is a diagram for explaining an operation of the wide angle image pickup apparatus according to a fourth embodiment of the present invention; and

FIG.10 is a diagram showing a write address and read address for a memory in the wide angle image pickup apparatus according to the fourth embodiment of the present invention.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

FIG.1 is a diagram for explaining a wide angle image pickup apparatus according to the first embodiment of the present invention. Reference numeral 1 denotes a solid-state image device (CCD) for converting an object image introduced through an image pickup lens (not shown) to a CCD signal (pixel signal) and outputting it. Reference numeral 2 denotes a signal processing circuit for carrying out a

predetermined signal processing such as noise removal and  
signal level adjustment to the CCD signal from the solid-  
state image device 1 and outputting it. Reference numerals  
3, 4 denote first and second memories in which a signal from  
5 the signal processing circuit 2 is written alternately and  
this signal is read out at a predetermined timing which will  
be described later. Reference numeral 5 denotes a selector  
for outputting selectively any of a signal read out from the  
first memory 3 and a signal read out from the second memory  
10 4.

A so-called progressive scan type solid-state image  
device that is one of the frame transfer types is employed  
as the solid-state image device 1 and so constructed that  
pixel data of a frame (a screen composed of pixel data  
15 corresponding to odd field and pixel data corresponding to  
even field) is outputted in the period of a field.

FIG.2 is a diagram showing an imaging plane of the  
solid-state image device 1, in which a camera shake correction  
region is provided around the standard image pickup region.  
20 In this camera shake correcting processing, camera shake  
correcting components are computed based on pixel data read  
out from the solid-state image device 1 and a read address  
of the image pickup device is changed horizontally and  
vertically based on this camera shake correcting components.

25 In the solid-state image device 1, assuming that the  
number of pixels to be arranged vertically in the standard  
image pickup region is "a" and the number of the pixels to  
be arranged horizontally is "b", the number of pixels in the  
vertical direction in the image pickup plane including the  
30 standard image pickup region and camera shake correction  
region is " $a \times (4/3)$ " and the number of pixels in the  
horizontal direction is " $b \times (4/3)$ ".

A feature of the wide angle image pickup apparatus of  
the present invention is that a wide angel image pickup is  
35 enabled using not only the pixel signal from the standard  
image pickup region of the solid-state image device 1 but also



the pixel signal from the camera shake correction region. Hereinafter, an operation of the wide angle image pickup apparatus of the present invention will be described with reference to FIGs.1-3. The solid-state image device 1 reads out the pixel data corresponding to "a" lines in the vertical direction shown in FIG.2 in the period of a field under normal image pickup mode and at the same time, carries out camera shake correcting processing as described above. On the contrary, under reduction image pickup mode, the pixel data corresponding to " $a \times (4/3)$ " lines including the camera shake correction region as shown in FIG.2 are read out in the period of 4/3 field.

That is, regardless of the normal image pickup mode and the reduction image pickup mode, the line reading speed in the vertical direction is the same when the pixel data is read out from the solid-state image device 1. Therefore, although read-out is enabled in the period of a field under the normal image pickup mode, the period of 4/3 field is required for the reduction image pickup mode.

FIG.3 shows a timing chart for use in taking pictures under the reduction image pickup mode. As shown in the Figure, the read-out of the pixel data is started at the timing of vertical synchronous signal when an image signal is outputted from the solid-state image device 1 as an animation. After the period of 4/3 field passes, the read-out of the pixel data corresponding to a screen is completed. Because the period of 4/3 field is required to read out the pixel data of a screen under the reduction image pickup mode, the pixel data corresponding to a screen cannot be outputted in every period of a single vertical synchronous signal and therefore, the pixel data corresponding to a screen is outputted in every two vertical synchronous signal.

In FIG.3, the pixel data of a first screen outputted from the solid-state image device 1 is D1, the pixel data of a second screen is D2 and the pixel data of a third screen is D3. These pixel data are subjected to a predetermined

signal processing by the signal processing circuit 2 as described above and after that, the pixel data are written into the first and second memories alternately so that the pixel data D1 is written to the first memory 3, the pixel data D2 is written to the second memory 4 and the pixel data D3 is written to the first memory 3.

Under the normal image pickup mode, the pixel data corresponding to the "a" lines in the vertical direction are read out from the solid-state image device 1. Under the reduction image pickup mode, the pixel data corresponding to " $a \times (4/3)$ " lines in the vertical direction are read out in the period of  $4/3$  field. Therefore, each of the first and second memories has a memory capacity capable of storing pixel data corresponding to a screen under the reduction image pickup mode ( $4/3$  data amount read out under the normal image pickup mode, that is, a memory capacity corresponding to data amount  $8/3$  times when the first and second memories are summed up).

Read-out of the pixel data D1 written in the first memory 3 is started at the timing of a next vertical synchronous signal after write of the pixel data D1 into the memory is completed. At that time, only the pixel data D1(odd) corresponding to odd field in the pixel data D1 corresponding to a single screen are selectively read out over the period of a field. After that, only the pixel data D1(even) corresponding to even field in the pixel data in the memory are read out selectively over the period of a field from a next timing of the vertical synchronous signal.

Then, the pixel data D2 corresponding to the second screen are written into the second memory 4 at the timing of starting read-out of the pixel data D1(odd) from the first memory 3. The read-out of the pixel data D2 corresponding to odd field in the pixel data D2 is started at the timing of the next vertical synchronous signal after write of the pixel data D2 of the second screen is completed, that is, at a timing when the read-out of the pixel data D1(even) from

the first memory 3 is completed, and then, the read-out of the pixel data D2(even) corresponding to even field is started successively.

Meanwhile, if when pixel data corresponding to the odd field or even field are read out from the first and second memories, all the pixel data in the vertical and horizontal directions written into the memory are read out, the numbers of the pixels in the vertical direction and horizontal direction are 4/3 times as compared to the normal image pickup mode. At this time, if the pixel data are read out by thinning in the vertical and horizontal directions, the pixel data of the same pixel quantity as the normal image pickup mode can be outputted.

It is needless to say that instead of this thinning processing, a more favorable wide angle image can be obtained by carrying out electronic zoom reduction processing at the same time when the pixel data is read out from the memory. As an example of such an electronic zoom reduction processing, a method for generating virtual pixel data at the same position in screen as the normal image pickup mode using multiple pixel data adjacent in the vertical and horizontal directions by means of linear interpolation or spline interpolation is available.

If it is intended to output pixel data read out from the solid-state image device 1 as a still picture, the pixel data corresponding to a screen written in the first or second memory only have to be outputted as they are and at that time, a still picture corresponding to XGA can be obtained.

Next, the wide angle image pickup apparatus according to a second embodiment of the present invention will be described.

FIG.4 is a diagram for explaining the wide angle image pickup apparatus according to the second embodiment. In this embodiment, instead of the first memory 3 and second memory 4 of the first embodiment, a third memory 6 is provided.

The solid-state image device 1 and signal processing

circuit 2 in the wide angle image pickup apparatus according to the second embodiment and following embodiments are the same as a structure of the wide angle image pickup apparatus of the first embodiment. Thus, only the third memory 6 will  
5 be described. Here, the third memory 6 has a memory capacity similar to data amount  $7/3$  times reading data amount under the normal image pickup mode.

FIG.5 is a timing chart for use in taking pictures under the reduction image pickup mode in the wide angle image pickup  
10 apparatus according to the second embodiment. FIG.6 is a diagram showing addresses upon writing and reading the pixel data in the third memory 6.

The pixel data D1 at the same timing as shown in FIG.3 are read from the solid-state image device 1 and after the  
15 period of  $4/3$  field passes, read-out of the pixel data D1 is completed. Then, the pixel data D1 outputted from the solid-state image device 1 are written into the third memory 6 at the same timing. Then, the pixel data D1(odd) corresponding to the odd field in the pixel data D1 are read  
20 out at a timing of a next vertical synchronous signal and at the same time, the pixel data D2 are written into another region in the memory.

FIG.6 indicates a write address at that time. A state in which the pixel data D1 are written in the period of  $4/3$   
25 field is indicated by W1, a state in which the pixel data D1(odd) corresponding to odd field in this pixel data D1 are read out in the period of a field is indicated by R1o, a state in which the pixel data corresponding to even field in the pixel data D1(even) are read out in the period of a field is  
30 indicated by R1e, and a state in which the pixel data D2 are written in the period of  $4/3$  field is indicated by W2 and W2'.

If the write (W1) of the pixel data D1 is completed, a read-out (R1o) of the pixel data D1(odd) is started at a next vertical synchronous signal timing and then, a write (W2)  
35 of the pixel data D2 in another region of the memory is started. The pixel data D2 are written into other region without being

written over the pixel data D1 already written in the memory,  
at least until the read-out (R1o) of the pixel data D1 (odd)  
is completed (W2). Thus, the third memory 6 needs to have  
a memory capacity corresponding to data amount 7/3 times the  
5 reading data amount under the normal image pickup mode.

Then, the read (R1e) of the pixel data D1 (even) is  
started after the read (R1o) of the pixel data D1 (odd) is  
completed. At this time, a remainder of the pixel data D2  
is written into the address region of the pixel data D1 already  
10 read out (W2').

Because the memory corresponding to data amount 7/3  
times data amount read out under the normal image pickup mode  
is used and the startup of the pixel data D2 is set up just  
after the startup of the read-out of the pixel data D1(even),  
15 the memory capacity can be reduced as compared to the wide  
angle image pickup apparatus of the first embodiment.

Next, the wide angle image pickup apparatus according  
to a third embodiment of the present invention will be  
described below.

20 In the wide angle image pickup apparatus of the third  
embodiment, the memory capacity is reduced further as  
compared to the third memory 6 of the wide angle image pickup  
apparatus of the second embodiment. The memory needs to have  
a capacity corresponding to 5/3 times the reading data amount  
25 under the normal image pickup mode.

FIG.7 shows a timing chart for use in taking pictures  
under the reduction image pickup mode in the wide angle image  
pickup apparatus of the third embodiment. FIG.8 is a diagram  
showing addresses upon write and read of the pixel data in  
30 the third memory 6.

In the wide angle image pickup apparatus of this  
embodiment, as shown in FIG.7, the read-out of the pixel data  
from the solid-state image device 1 is not started at the  
timing of the vertical synchronous signal, but the read-out  
35 of the pixel data is started at such a timing that a completion  
of reading of the pixel data from the solid-state image device

1 coincides with the vertical synchronous signal timing. That is, the reading of the pixel data is started later by the period of 2/3 field than the vertical synchronous signal timing.

5 If the reading of the pixel data D1 from the solid-state image device 1 is started, after the period of 4/3 field passes, the reading of the pixel data D1 corresponding to a screen is completed. Then, the pixel data D1 outputted from the solid-state image device 1 are written into the third  
10 memory 6 at the same timing as this reading and then, the reading of the pixel data D1(odd) corresponding to the odd field in the pixel data D1 is started at the timing of the vertical synchronous signal, the timing at which the writing of the pixel data D1 is just completed. Then, after the period  
15 of 2/3 field passes, the writing of the pixel data D2 into other region is started.

FIG.8 shows a write address and a read address at that time. In this Figure, a state in which the pixel data D1 are written in the period of 4/3 field is indicated by W1, a state  
20 in which the pixel data D1(odd) corresponding to odd field in the pixel data D1 are read out in the period of a field is indicated by R1o, a state in which the pixel data D1 (even) corresponding to even field in the pixel data D1 are read out in the period of a field is indicated by R1e, and a state in  
25 which the pixel data D2 are written in the period of 4/3 field is indicated by W2 and W2'.

If the writing (W1) of the pixel data D1 is completed, the reading (R1o) of the pixel data D1 (odd) is started at the same timing. Then, after a period of 2/3 field passes  
30 after the reading of the pixel data D1 (odd) is started, the writing (W2) of the pixel data D2 in other region of the memory is started.

At least until the reading of the pixel data D1(odd) is completed with this condition, the pixel data D2 are  
35 written into other region without being written over the pixel data D1 written in the memory (W2). Thus, the third memory

needs to have a memory capacity corresponding to data amount 5/3 times the reading data amount under the normal image pickup mode.

Then, after the reading of the pixel data D1(odd) is completed, the reading of the pixel data D1(even) is started. At that time, a remainder of the pixel data D2 is written into an address region of the pixel data D1 already read out (W2').

By shifting the write timing of the pixel data into the memory, the memory capacity can be further reduced as compared to the wide angle image pickup apparatus of the first and second embodiments.

Next, the wide angle image pickup apparatus according to a fourth embodiment of the present invention will be described.

In the wide angle image pickup apparatus of the fourth embodiment, the memory capacity is reduced further as compared to the above described embodiments. In this case, a memory corresponding to data amount 4/3 times the reading data amount under the normal image pickup mode is used.

FIG.9 is a timing chart for use in taking pictures under the reduction image pickup mode in the wide angle image pickup apparatus of the fourth embodiment. FIG.10 is a diagram showing a write address and a read address of the pixel data in the third memory 6.

The reading of the pixel data D1 from the solid-state image device 1 is started at the timing of the vertical synchronous signal and after a period of 4/3 field passes, the reading of the pixel data D1 corresponding to a screen is completed. Then, the pixel data D1 outputted from the solid-state image device 1 are written into the third memory 6 at the same timing as reading. Then, the reading of the pixel data D1(odd) corresponding to odd field in the pixel data D1 is started at the timing of a vertical synchronous signal next to the vertical synchronous signal which has started writing of the pixel data D1.

Then, reading of the pixel data D1(even) corresponding

to even field is started at the timing that the reading of the pixel data D1(odd) corresponding to odd field is completed and then, the writing of the pixel data D2 is started just after the reading of the pixel data D1(even) is started.

5        FIG.10 shows write address and read address at that time. A state in which the pixel data D1 are written in the period of 4/3 field is indicated by W1, a state in which the pixel data D1(odd) corresponding to odd field in this pixel data D1 are read in the period of a field is indicated by R1o, a  
10       state in which the pixel data D1(even) corresponding to even field in the pixel data D1 are read in the period of a field is indicated by R1e and a state in which the pixel data D2 are written in the period of 4/3 field is indicated by W2.

15       In the wide angle image pickup apparatus of this embodiment, the writing (W1) of the pixel data D1 is started and at the timing of a next vertical synchronous signal, the reading (R1o) of the pixel data D1 (odd) is started. Because the pixel data D1 at a region which should be read out has been already written into the memory although the writing of  
20       the pixel data D1 is not completed, this is not a problem for carrying out the reading.

25       If the reading (R1o) of the pixel data D1(odd) is completed, the reading (R1e) of the pixel data D1(even) is started subsequently. Just after the reading of the pixel data D1(even) is started, the writing (W2) of the pixel data D2 is started.

30       As described above, in the wide angle image pickup apparatus of this embodiment, the reading of the pixel data D1(odd) is started at the timing of a vertical synchronous signal next to a vertical synchronous signal which has started the writing of the pixel data D1. As a result, a wide angle image can be obtained only with a memory capacity capable of storing the pixel data corresponding to a single screen under the reduction image pickup mode.

35       According to the second to fourth embodiments described above, by providing the respective memory capacities with



some margins, it is possible to prevent a phenomenon that the writing timing of the pixel data into the memory is accelerated with respect to the reading timing of the pixel data.

5           That is, in FIGs.6 and 8, by providing the memory capacity with some margins, the writing amount of the pixel data D2 by W2 can be set larger than an indicated state. As a result, the writing start timing by W2' can be retarded sufficiently as compared to the reading (R1e) start timing  
10 of the pixel data D1(even).

In this particular case, it is needless to say that the timing at which the writing (W2') is started corresponds to the timing at which the writing (W2) is completed.

15           In FIG.10, by providing the memory capacity with some margins, the writing of the pixel data D2 by W2 is changed like W2". As a result, the writing start timing by W2" can be retarded sufficiently as compared to the reading (R1e) start timing of the pixel data D1(even).

20           In the above description, all the pixel data of "a" lines read out from the solid-state image device 1 are written into a memory so as to form a squeeze image when the pixel data are read out from the memory under the normal image pickup mode. If it is not necessary to output as the squeeze image, it is permissible to read out the pixel data in the standard  
25 image pickup region from the solid-state image device 1 and write it into the memory.

30           According to the wide angle image pickup apparatus of the present invention, the video image signal is generated using not only the image pickup device in the standard image pickup region of the solid-state image device but also the image pickup device in the peripheral image pickup region. Thus, if this is applied to a camera integrated type VTR, the angle of view on the side of a wide angle can be enlarged easily and an enlargement of the optical zoom lens can be avoided.

35           It should be understood that many modifications and adaptations of the invention will become apparent to those

skilled in the art and it is intended to encompass such obvious modifications and changes in the scope of the claims appended hereto.

What is claimed is:

1. A wide angle image pickup apparatus comprising:  
a solid-state image device containing a standard image  
5 pickup region in which image pickup devices of a number  
corresponding to the number of horizontal scanning lines of  
a video image signal in accordance with standard television  
system are arranged in a vertical direction and a peripheral  
image pickup region which is provided at least on vertically  
10 top and vertically bottom of said standard image pickup region  
and in which multiple image pickup devices are arranged in  
the vertical direction; and

a reduction processing means for reducing a pixel  
signal outputted from the standard image pickup region and  
15 peripheral image pickup region of said solid-state image  
device so as to output a video signal having said number of  
the horizontal scanning lines.

2. A wide angle image pickup apparatus according to  
20 claim 1 wherein said solid-state image device reads out a  
pixel signal from said standard image pickup region and  
peripheral image pickup region and

said reduction processing means contains a memory means  
in which a pixel signal outputted from said solid-state image  
25 device is written and read out synchronously with a vertical  
synchronous signal of said video signal.

3. A wide angle image pickup apparatus according to  
claim 1 further including normal image pickup mode and  
30 reduction image pickup mode, wherein

under said normal image pickup mode, a pixel signal  
outputted from the standard image pickup region of said  
solid-state image device is outputted as said video signal  
without said reduction processing by said reduction  
35 processing means, and

under said reduction image pickup mode, a pixel signal

outputted from the standard image pickup region and peripheral image pickup region of said solid-state image device is outputted as said video signal with said reduction processing by said reduction processing means.

5

4. A wide angle image pickup apparatus according to claim 3 wherein said memory means is capable of carrying out write and read of said pixel signal at the same time and under said reduction image pickup mode, said solid-state image device starts output of pixel signals corresponding to a screen at such a timing that a output completion timing of the pixel signal of a screen coincides with a timing of a vertical synchronous signal of said video signal.

15 5. A wide angle image pickup apparatus having a solid-state image device containing a standard image pickup region in which image pickup devices of a number corresponding to the number of horizontal scanning lines of a video image signal in accordance with standard television system are arranged in a vertical direction and a peripheral image pickup region which is provided at least on vertically top and vertically bottom of said standard image pickup region and in which multiple image pickup devices are arranged in the vertical direction,

25 said wide angle image pickup apparatus further containing a moving image output mode for outputting said video signal which is a moving image based on a pixel signal outputted from at least the standard image pickup region of said solid-state image device and a still image output mode for outputting pixel signals corresponding to a screen outputted from the standard image pickup region and peripheral image pickup region of said solid-state image device.

ABSTRACT OF THE DISCLOSURE

A pixel signal outputted from an image pickup device  
in a standard image pickup region of a solid-state image  
5 device and a pixel signal outputted from an image pickup  
device in a camera shake correction region are written into  
a first memory and a second memory alternately for each screen.  
By adjusting the number of lines of a video signal upon reading  
out a signal from the memory, a moving image is outputted as  
10 a wide angle image. Further, by outputting all line signals  
in the memory, a wider angle still image is obtained.

FIG. 1

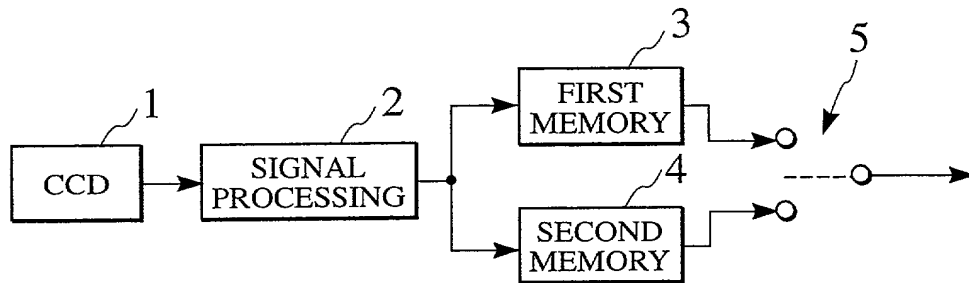


FIG. 2

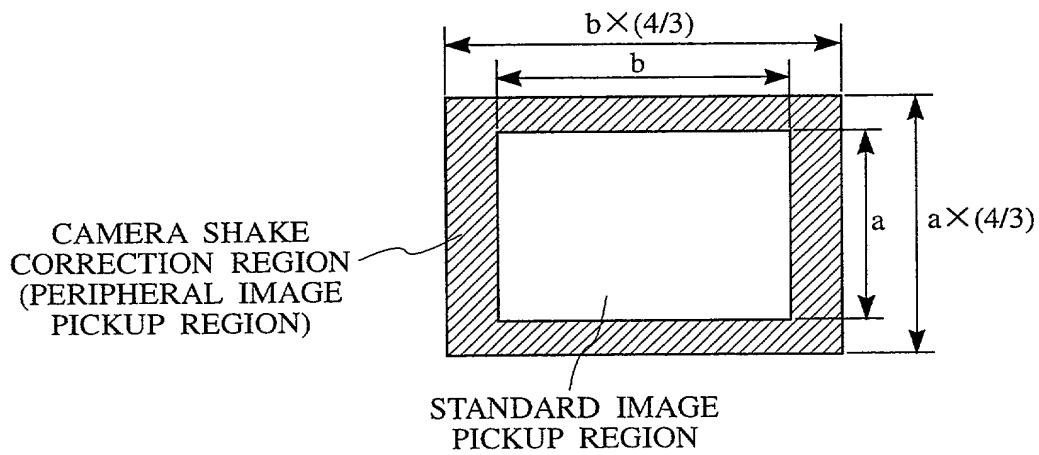


FIG. 3

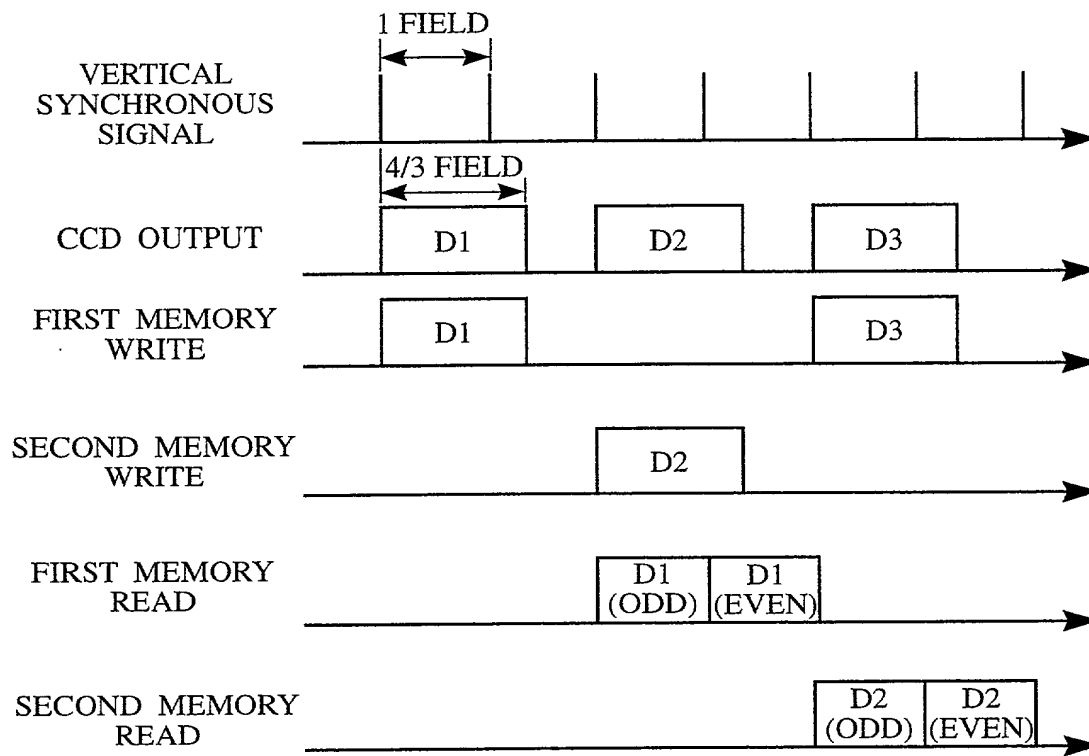


FIG. 4

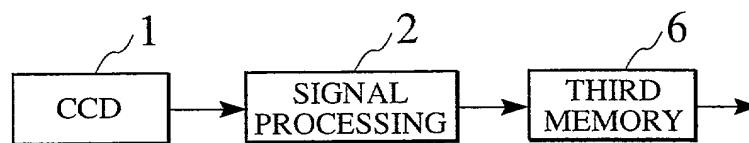


FIG. 5

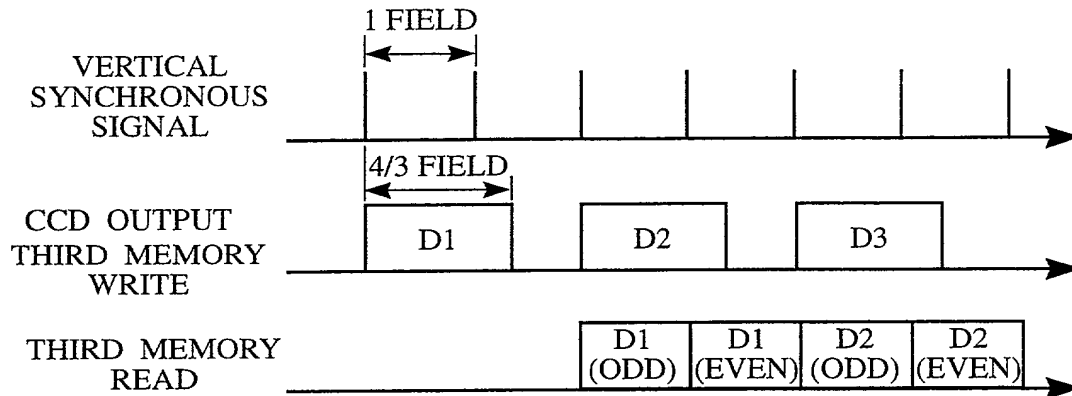


FIG. 6

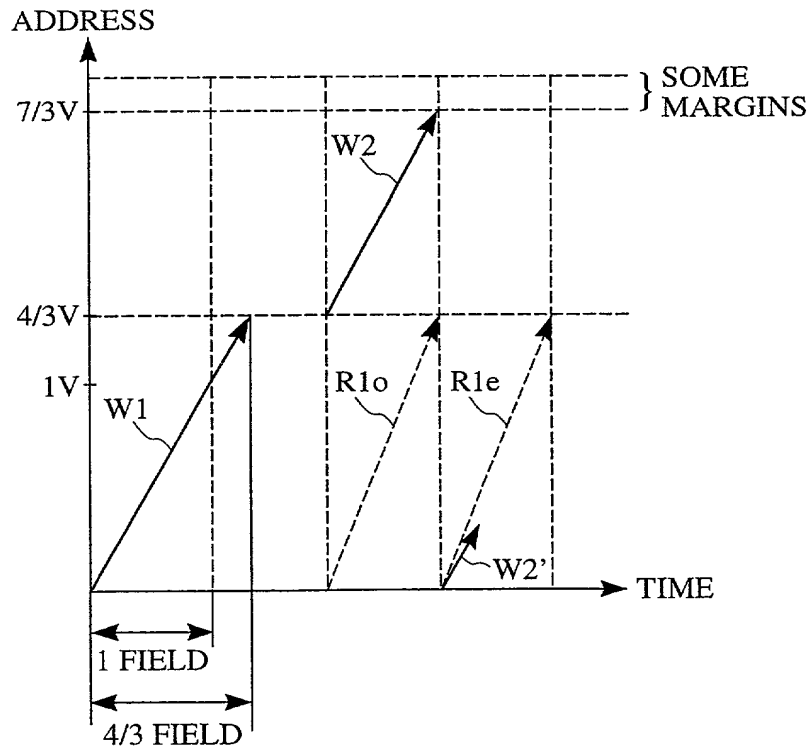




FIG. 7

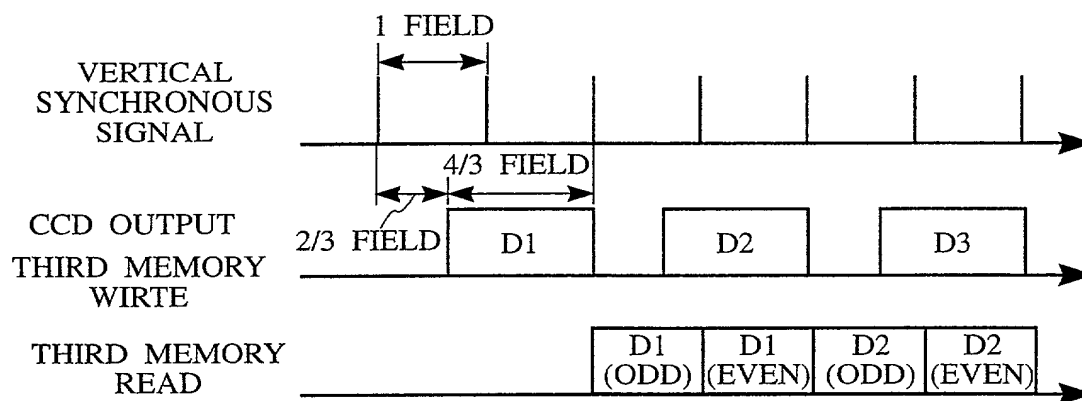


FIG. 8

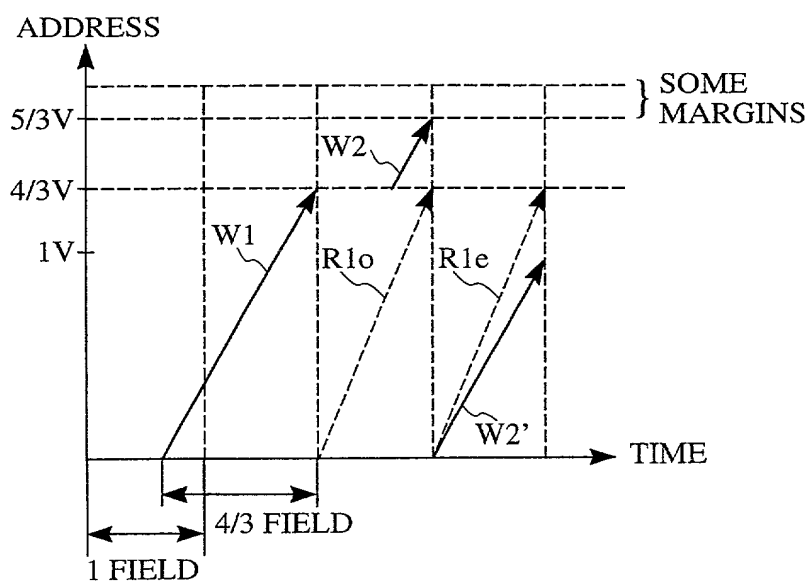


FIG. 9

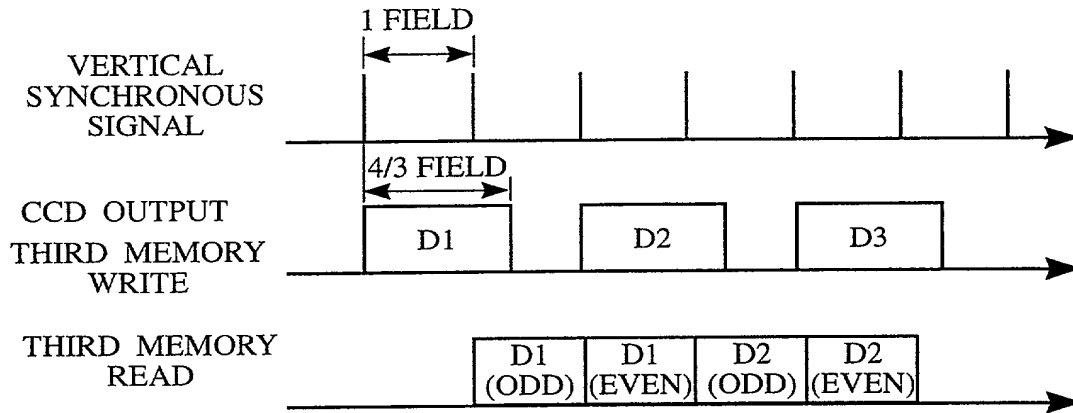
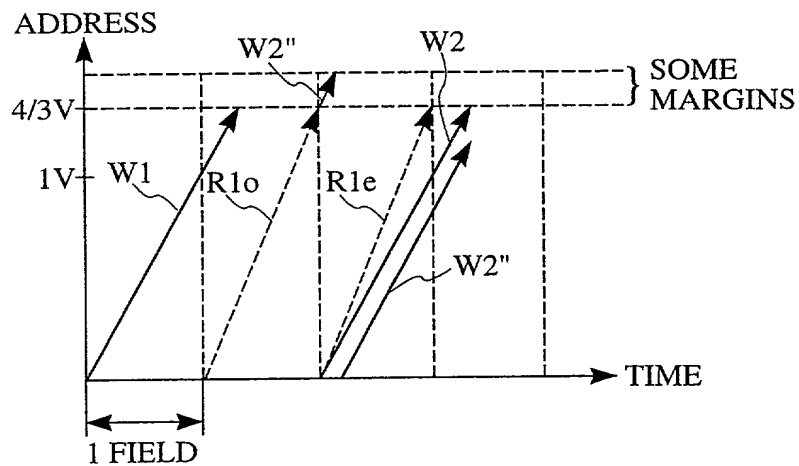


FIG. 10



## DECLARATION FOR PATENT APPLICATION

Attorney Docket 24369

As a below-named inventor(s), I/we hereby declare that:

My/Our residence(s), post office address(es) and citizenship(s) is/are as stated below next to my/our name(s).

I/We believe I/we am/are the original inventor, first and sole (if only one name is listed below) or the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed, and for which a patent is sought on the invention entitled:

## WIDE ANGLE IMAGE PICKUP APPARATUS

the specification of which: (check one)

☒ is attached hereto.

☐ was filed on \_\_\_\_\_, as Serial No. \_\_\_\_\_,

and was amended on \_\_\_\_\_ (if applicable).

We hereby state that we have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

We acknowledge the duty to disclose information which is material to the patentability of this application as defined by 37 CFR § 1.56.

We hereby claim foreign priority benefits under 35 U.S.C. § 119 of any foreign application(s) for patent or inventor's certificate listed below, and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

## Prior Foreign Applications:

P11-251285	Japan	06/09/1999	Priority Claimed
(Application No.)	(Country)	(Day/Month/Year Filed)	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
_____	_____	____/____/____	<input type="checkbox"/> Yes <input type="checkbox"/> No
(Application No.)	(Country)	(Day/Month/Year Filed)	
_____	_____	____/____/____	<input type="checkbox"/> Yes <input type="checkbox"/> No
(Application No.)	(Country)	(Day/Month/Year Filed)	

We hereby appoint Gary M. Nath, Reg. No. 26,965; Harold L. Novick, Reg. No. 26,011; Suet M. Chong, Reg. No. 38,104; Todd L. Juneau, Reg. No. 40,669; Lee C. Heiman, Reg. No. 41,827; Jerald L. Meyer, Reg. No. 41,194; Joshua B. Goldberg, Reg. No. 44,126; David Milligan, Reg. No. 42,893; David R. Murphy, Reg. No. 22,751; Paul A. Sacher, Reg. No. 43,418; Gregory B. Kang, Reg. No. 45,273; Scott F. Yarnell, Reg. No. 45,245; David R. Murphy, Reg. No. 22,751 and Robert G. Lev, Reg. No. 30,280, as my attorneys to prosecute this application and transact all business in the U.S. Patent and Trademark Office connected therewith.

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We hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by 35 U.S.C. § 112, first paragraph, I/we acknowledge the duty to disclose material information as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(U.S. Application Serial No.)	(U.S. Filing Date)	(Status--patented, pending, abandoned)
_____	_____	_____
(U.S. Application Serial No.)	(U.S. Filing Date)	(Status--patented, pending, abandoned)
_____	_____	_____

## DECLARATION FOR PATENT APPLICATION

Attorney Docket 24369

We hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor: Kenji MATSUOKAInventor's Signature Kenji Matsuoka Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 19-20-220, Yanagi-cho, Kanazawa-ku, Yokohama-shi, Kanagawa-ken, JapanFull name of second inventor: Kenji TAGUCHIInventor's Signature Kenji Taguchi Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 3-2-33, Kitaterao-cho, Tsurumi-ku, Yokohama-shi, Kanagawa-ken, JapanFull name of third inventor: Nobuyuki MATSUKAWAInventor's Signature Nobuyuki Matsukawa Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 1-25-18-101, Kubiri, Yokosuka-shi, Kanagawa-ken, JapanFull name of fourth inventor: Hidehiro KATOHInventor's Signature Hidehiro Katoh Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 549-7, Ikegami, Ohta-ku, Tokyo, JapanFull name of fifth inventor: Hiroyuki MIYAHARAInventor's Signature Hiroyuki Miyahara Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 2-1-14-C202, Motoohashi, Sakae-ku, Yokohama-shi, Kanagawa-ken, Japan

## DECLARAION FOR PATENT APPLICATION

Attorney Docket 24369

Full name of sixth inventor: Michihiro ASOInvestor's Signature Michihiro Aso Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 1-3-18, Nakashirane, Asahi-ku, Yokohama-shi, Kanagawa-ken, JapanFull name of seventh inventor: Takayuki OHTSUKAInvestor's Signature Takayuki Ohtsuka Date August 23, 2000Residence: Kanagawa-ken, JapanCitizenship: JapanPost Office Address: 796-33, Nakayama-cho, Midori-ku, Yokohama-shi, Kanagawa-ken, Japan